

**Errata Information**  
**Printed Circuit Board Design Techniques for EMC Compliance**  
**Second Edition**

**Last update: January 14, 2001**

To ascertain which printing the book is in, refer to the Library of Congress catalog page. In the middle of the page is a series of numbers. The lowest number indicates the print run. If a "1" appears, this is the original copy. If the number "1" is missing and the smallest number is a "2," then this is a second printing to incorporate errors detailed below.

The following changes are to be incorporated into the **2<sup>nd</sup> printing**. Grammatical, syntax or formatting changes not provided herein, only technical material important to the reader.

**Pg. 43**, Grounding Methodologies, 2<sup>nd</sup> line  
Change "funtion" to "function"

**Pg. 103**, Eq. (4.5)

Change: 
$$C_0 = \left( \frac{1}{H+T} \right) \ln \left( 1 - \frac{0.6897(\epsilon_r + 1.41)}{\sqrt{\epsilon_r}} \right)$$

To: 
$$C_0 = \frac{1.41(\epsilon' r)}{\ln \left[ \frac{5.98H}{(0.8W + T)} \right]}$$

**Pg. 253**

Item #18 – 1<sup>st</sup> sentence. Change "0.25" to "0.025"

Change 2<sup>nd</sup> paragraph to read:

"Above this frequency range, use of discrete capacitors may not perform as well as buried capacitance in decoupling digital components (3.4.4)."